In the Abstract:

Please amend the Abstract with the following amended abstract:

A driving circuit of a liquid crystal display device ~~having~~ includes driver output lines connected to outputs of a data line driver, m pieces of block selection signal lines for sequentially selecting m pieces of blocks, and data lines for supplying data to a display area~~, and a~~. A switch ~~for~~ sequentially ~~connecting~~ connects an ith driver output line to ith, i+2jth, ..., and i+2j×(m-1)th data lines in response to signals on the m pieces of block selection signal lines when j is a positive integer smaller than m.